

FIG 1a

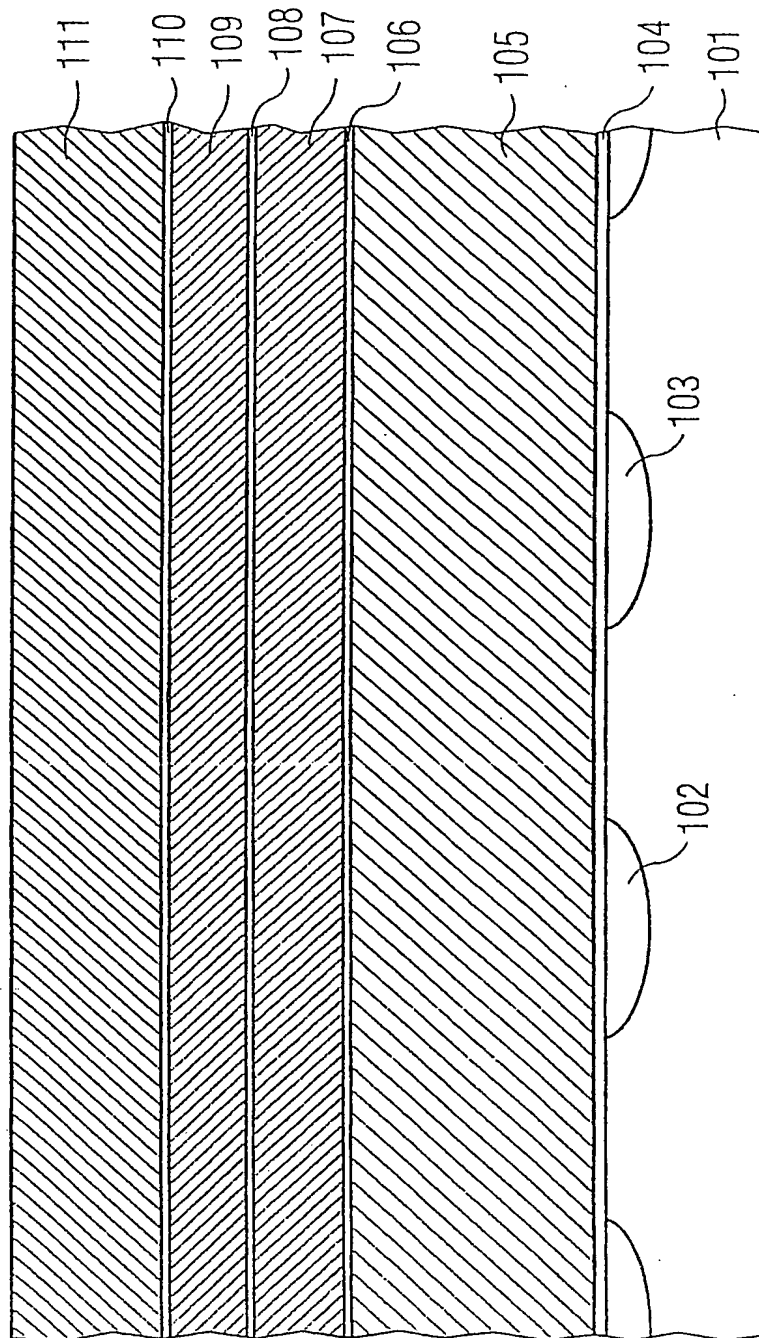


FIG 1b

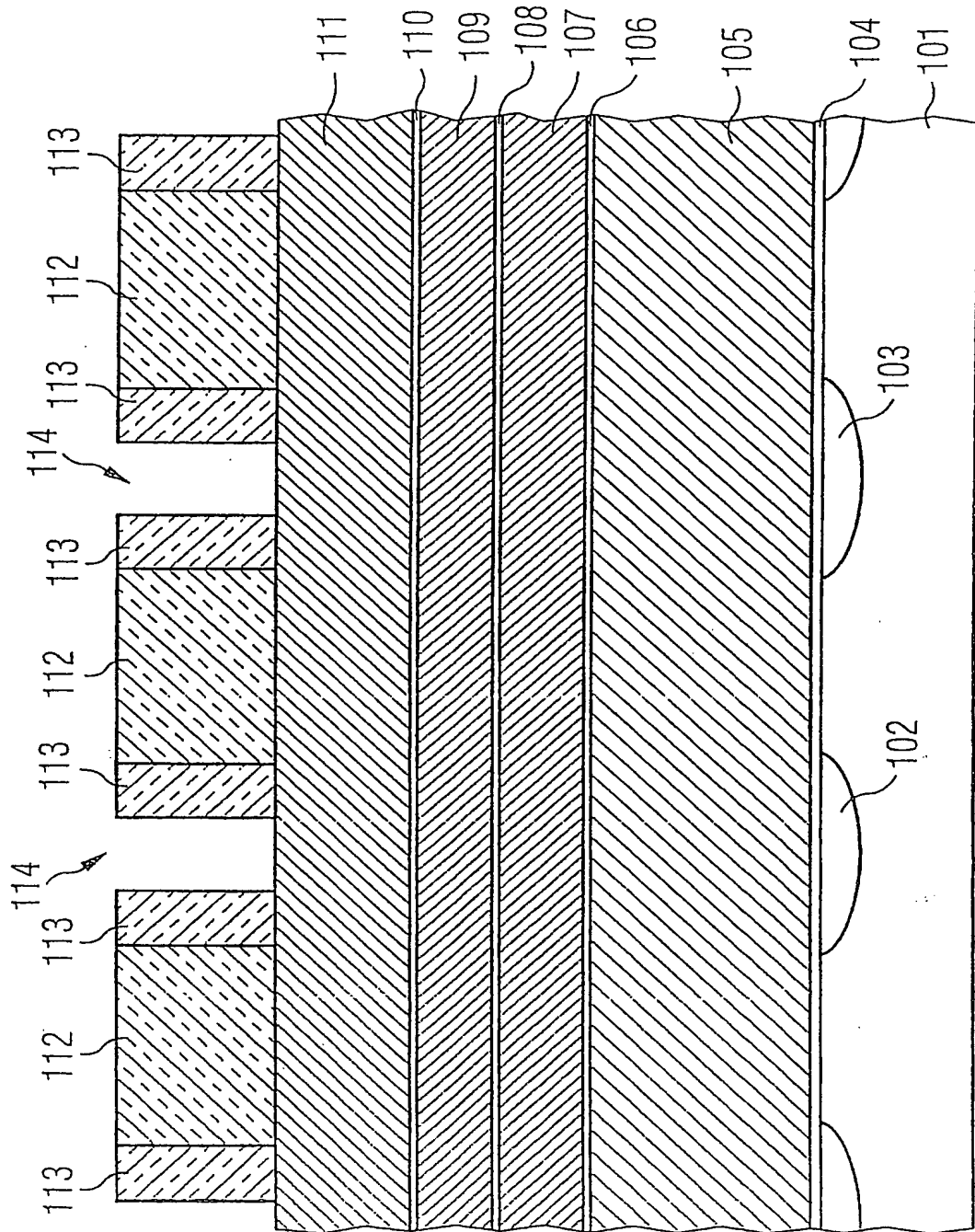


FIG 1c

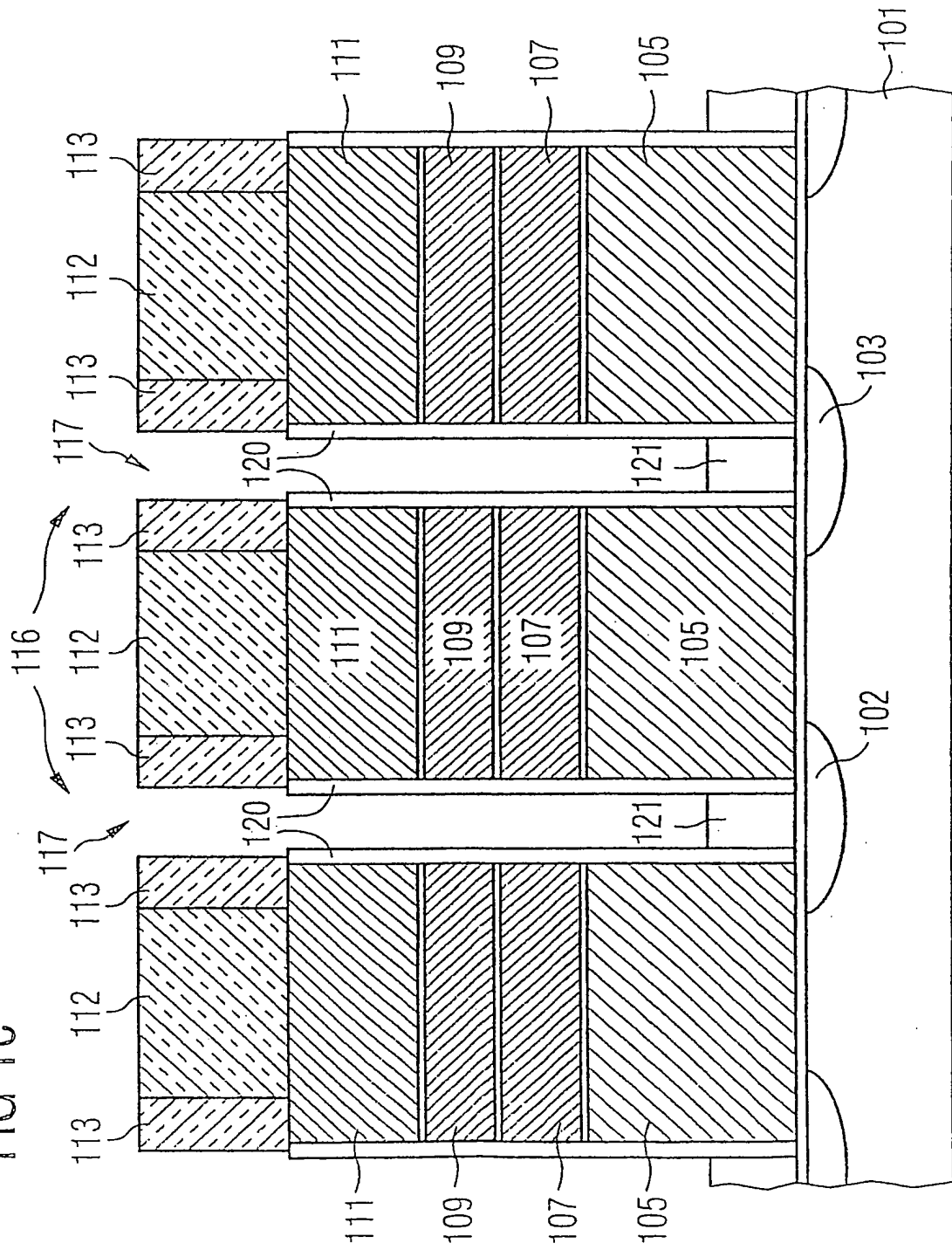


FIG 1d

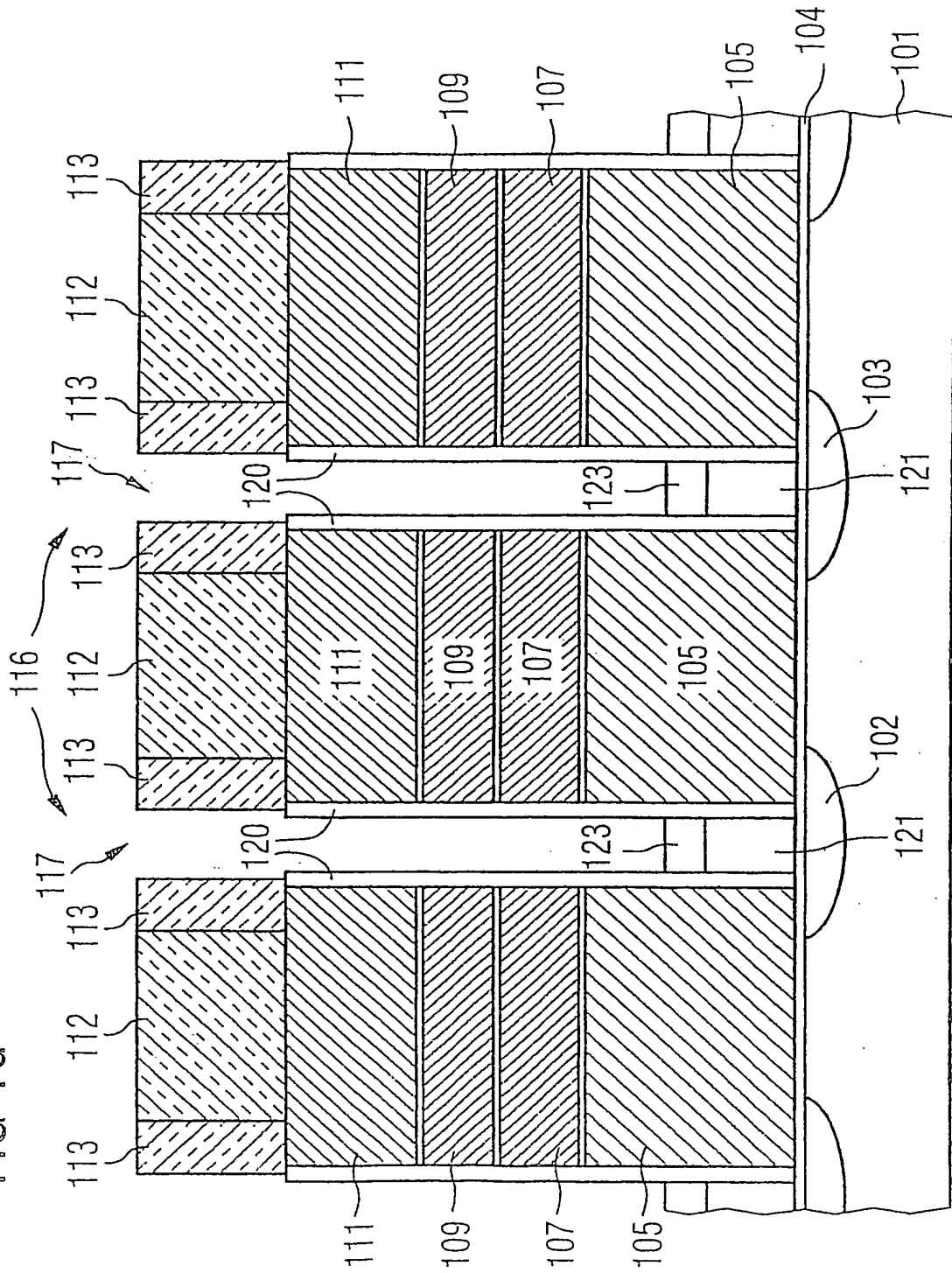


FIG 1e

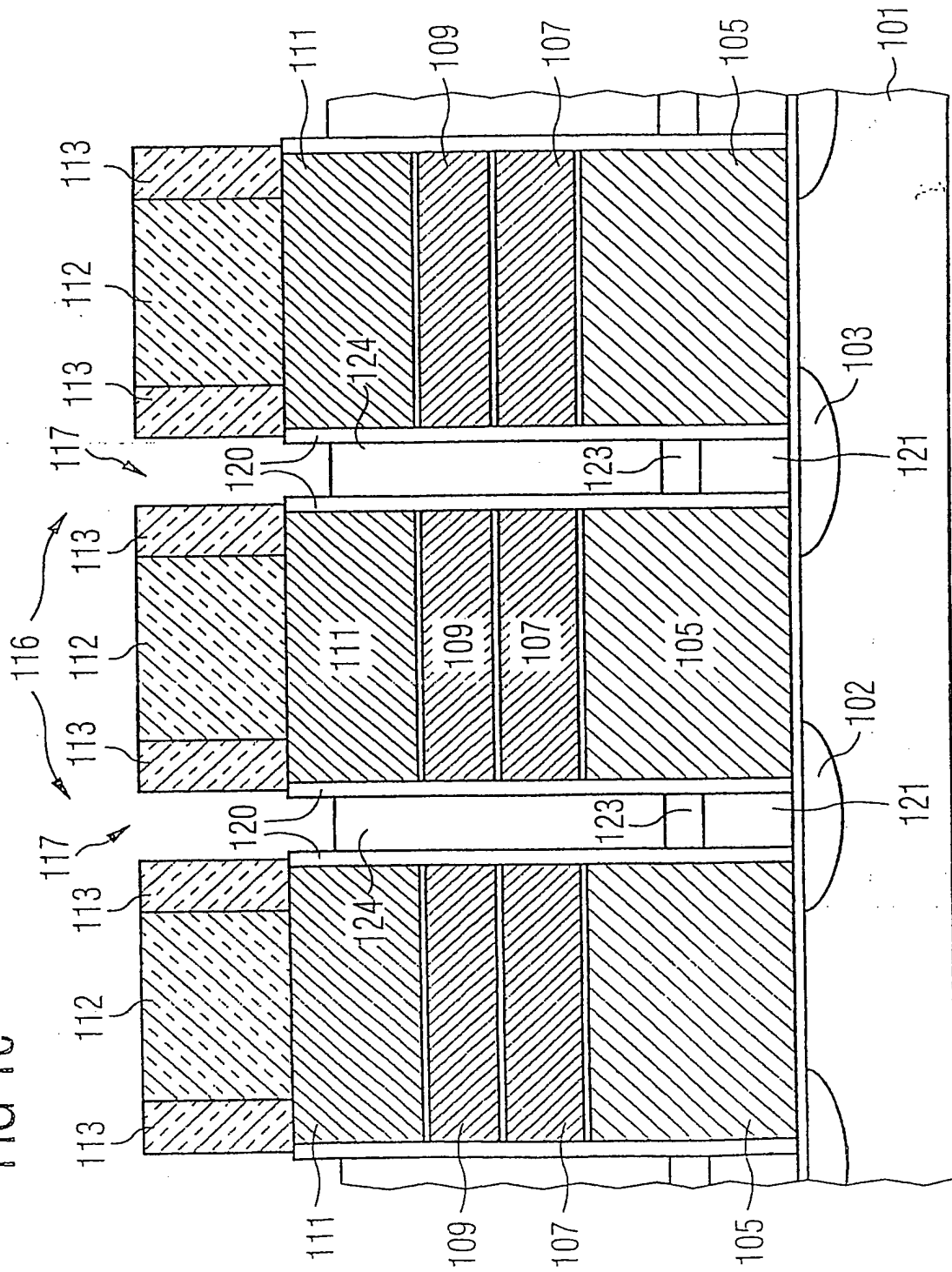


FIG 1f

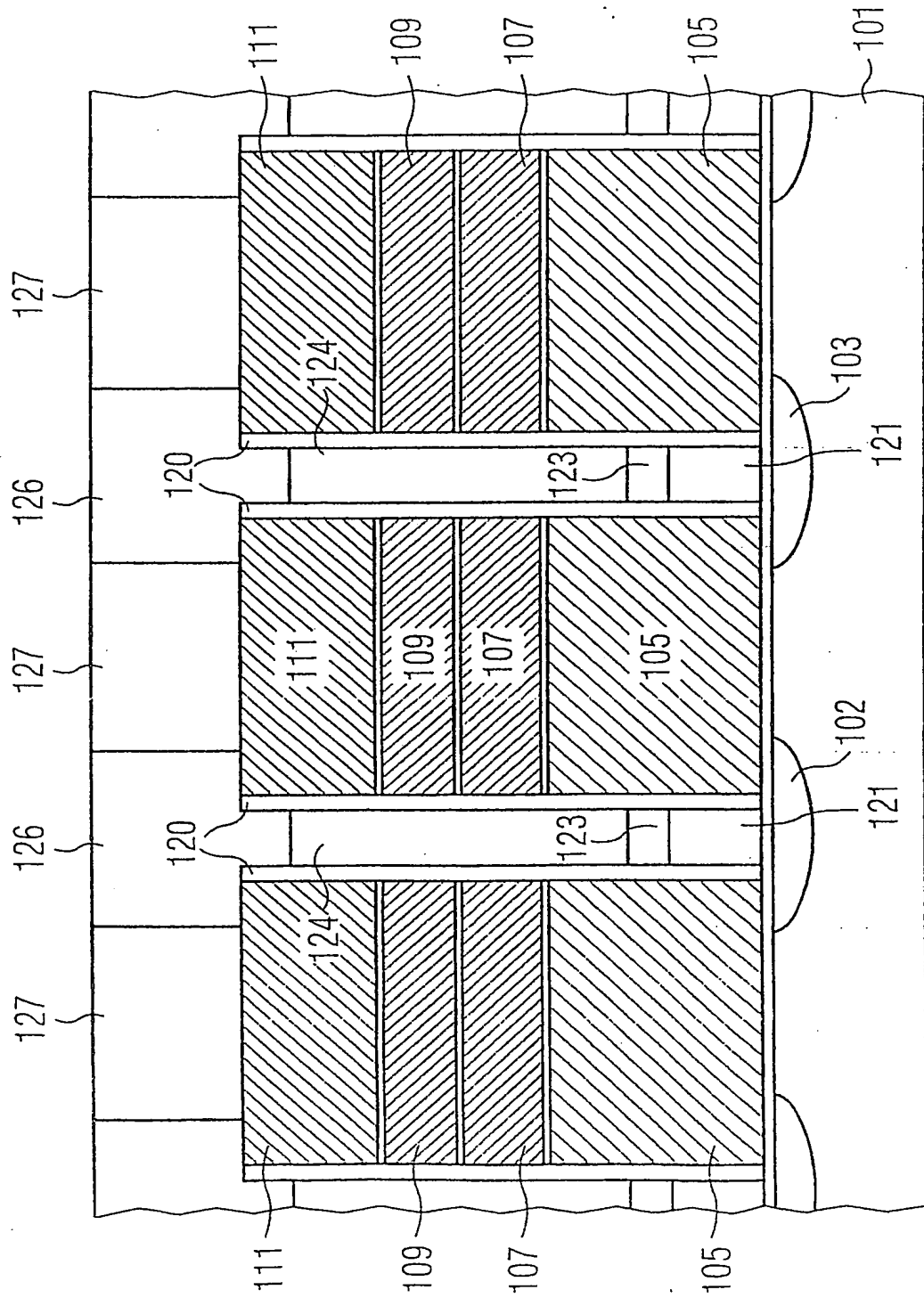


FIG 2a

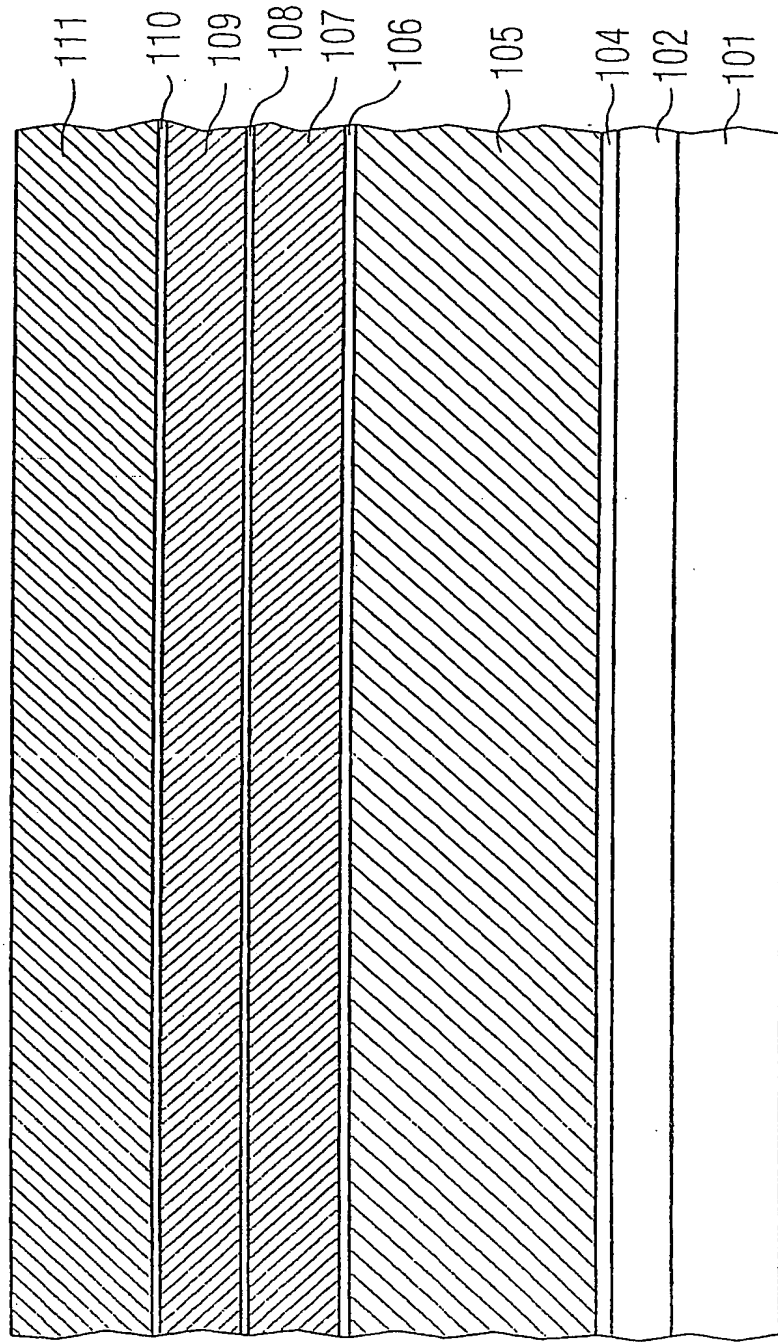
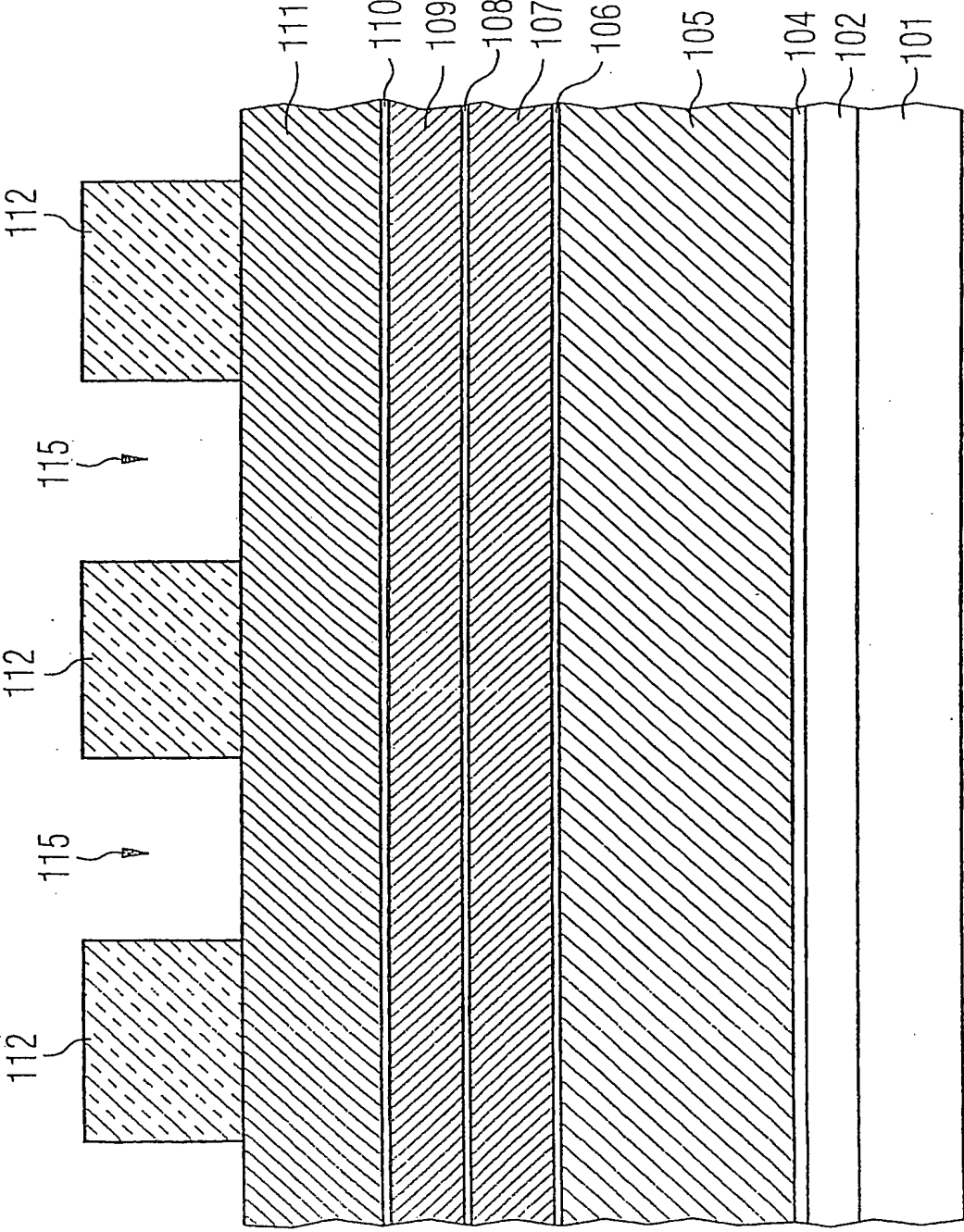


FIG 2b



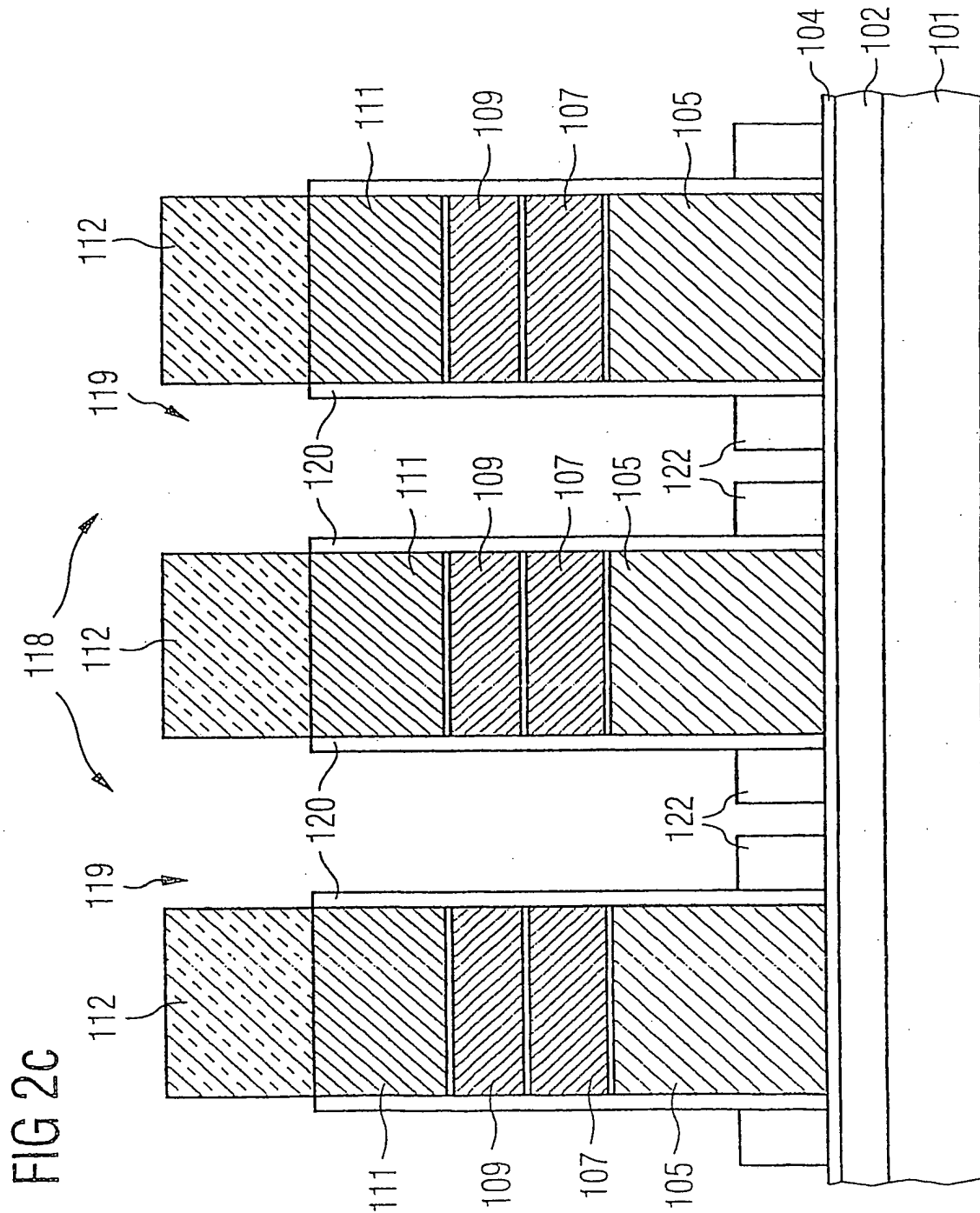


FIG 2d

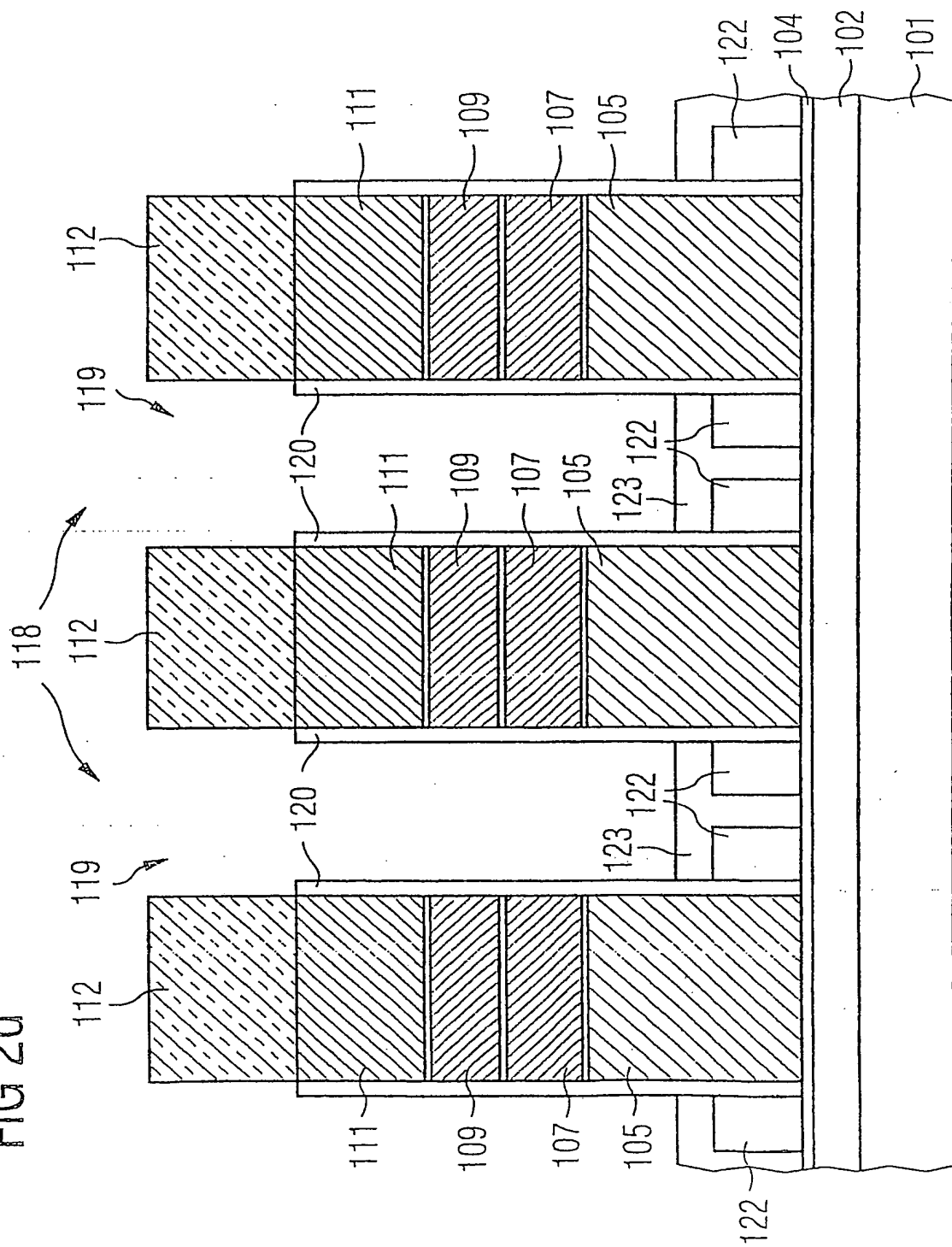


FIG 2e

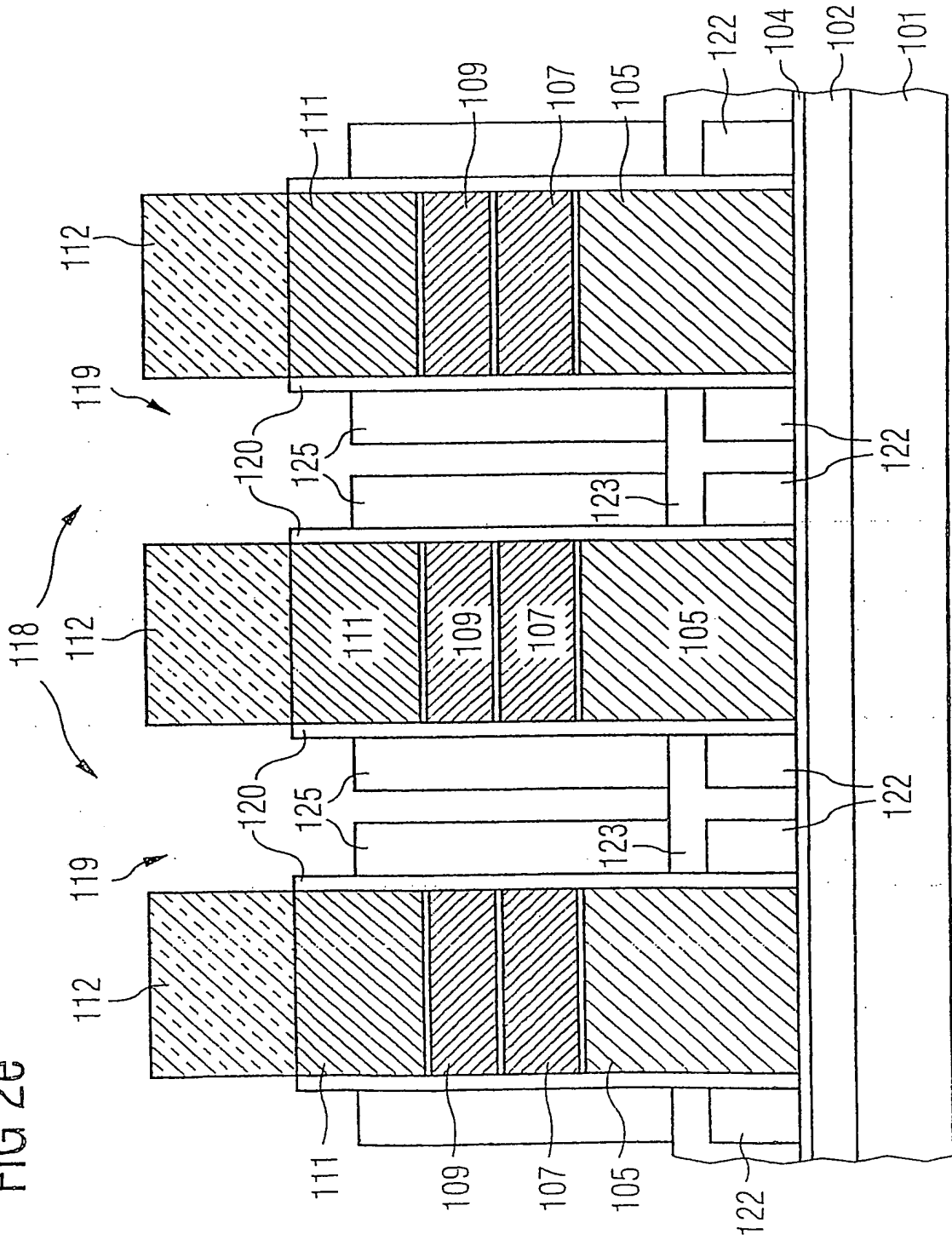


FIG 2f

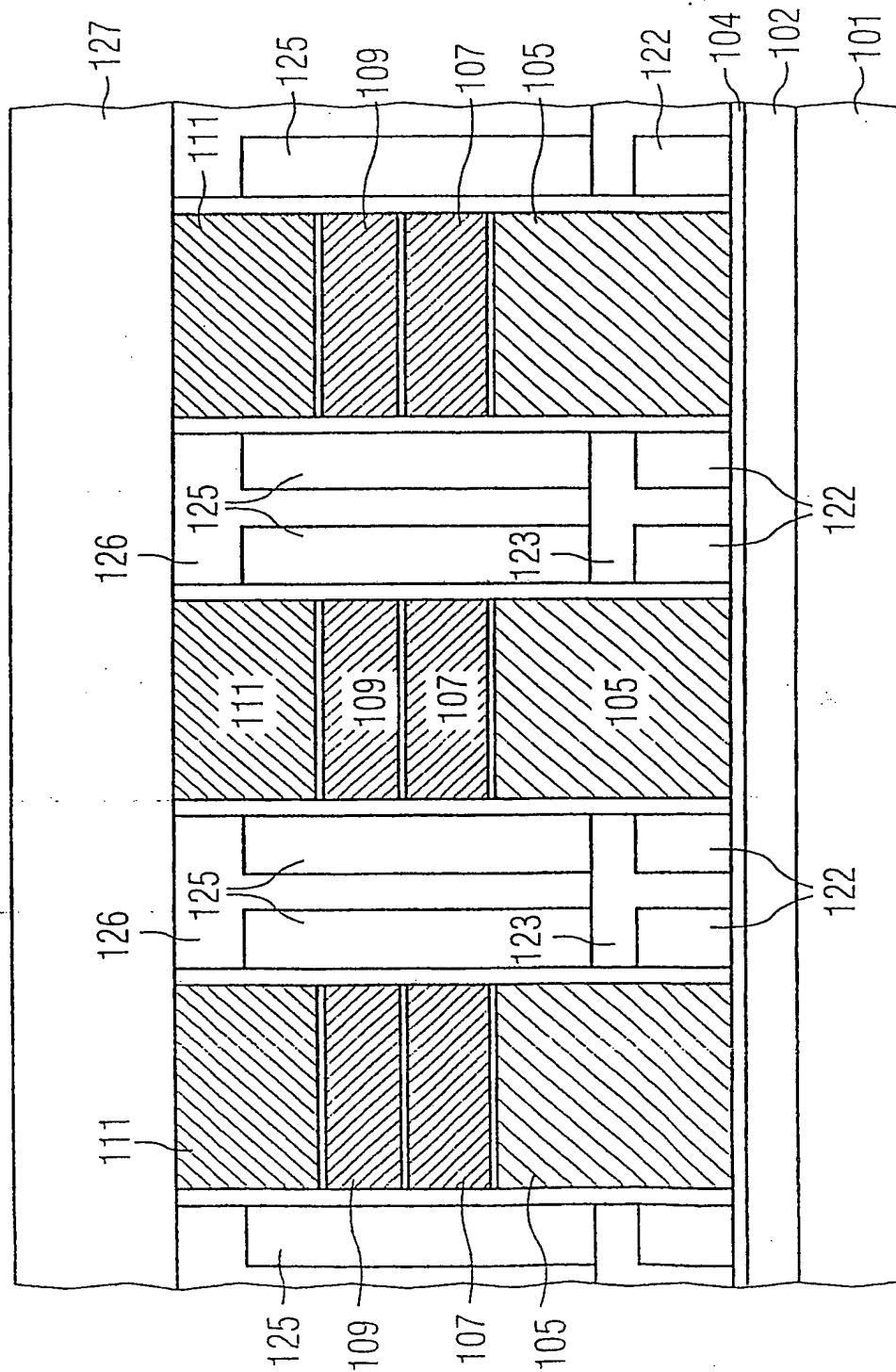


FIG 3

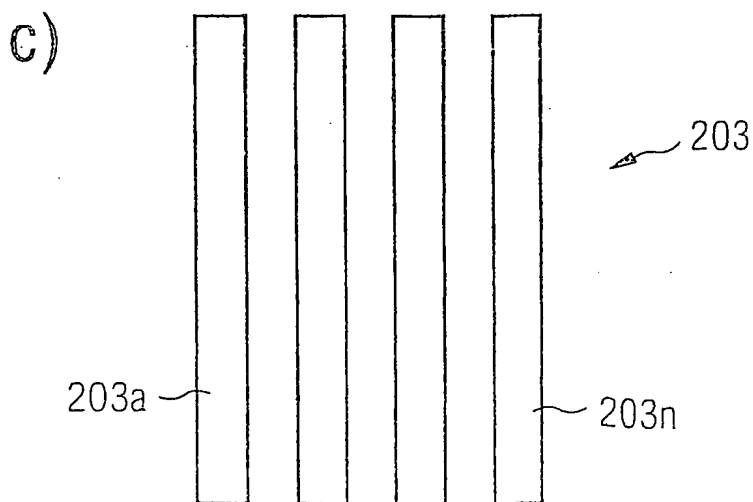
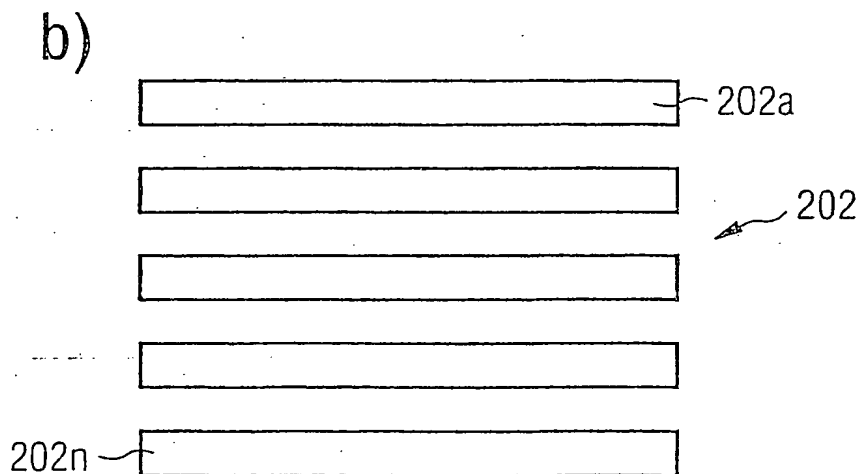
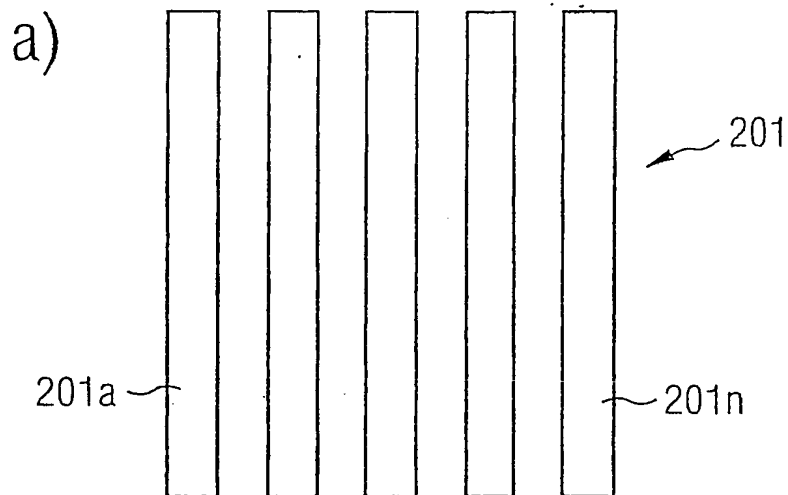


FIG 4

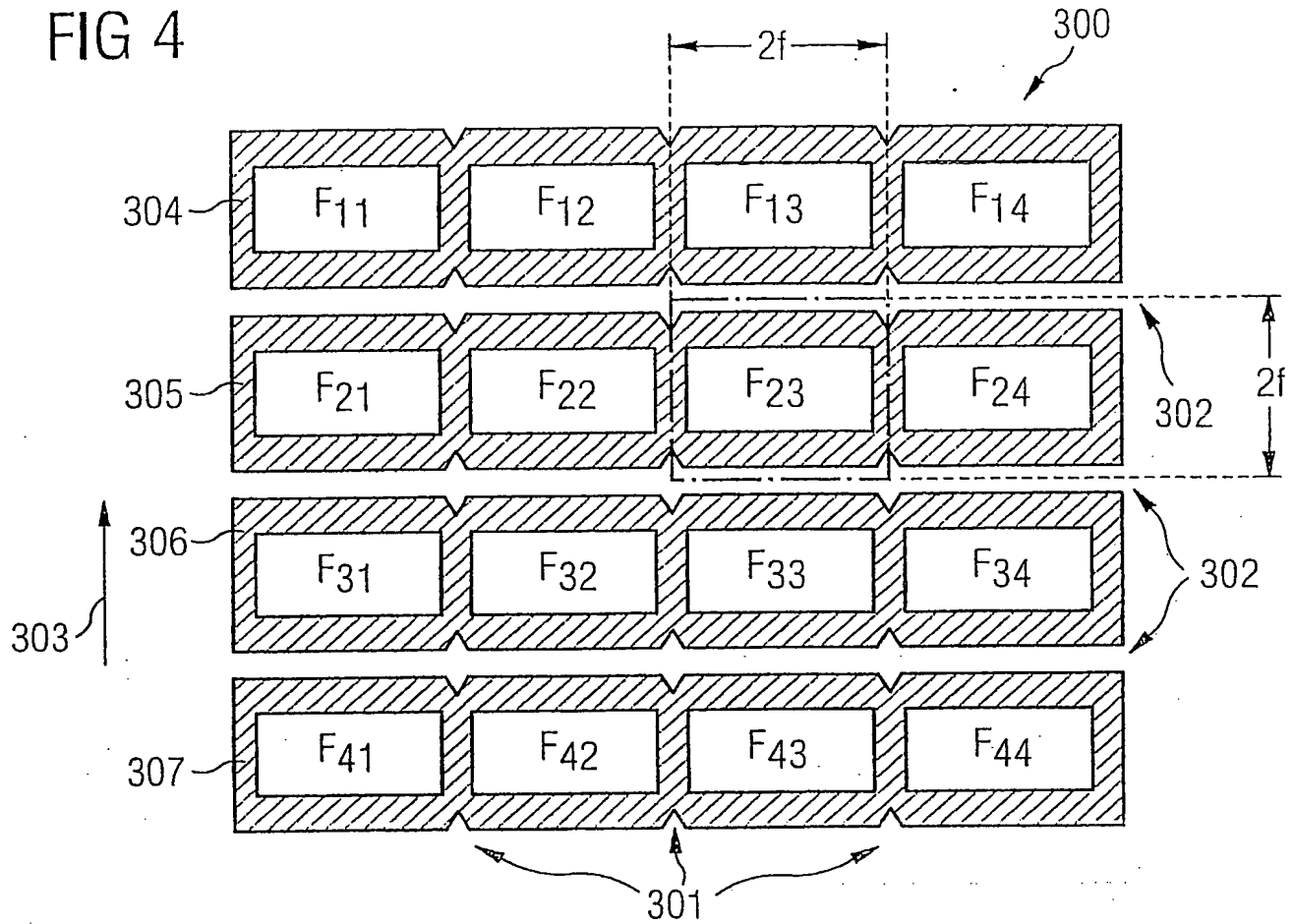


FIG 5

	Gate1	Gate2	Daten- leitung	Drain	Source
Schreiben	0V	+3V	-3V	0V	0V
Lesen	+3V	0V	0V	+2V	0V
Löschen	0V	+3V	+3V	0V	0V